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Sub	stitute for form 1449/P1	го		Application Number	10/589,399-Conf. #7126	
IN	IFORMATION	ON DI	SCLOSURE	Filing Date	August 14, 2006	
S	TATEMENT	ΓBY A	APPLICANT	First Named Inventor	Guo-Quan LU	
•	.,	. –		Art Unit	1793	
	(Use as many	/ sheets as	necessary)	Examiner Name	Y. Takeuchi	
Sheet	1	of	2	Attorney Docket Number	124617.0118	

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Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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JP-07126556-A	05-16-1995	Tatsuta Densen Kk		
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NON PATENT LITERATURE DOCUMENTS					
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	J	Wong Wai Kwan et al. "Low temperature sintering process for deposition of nano-structured metal for nano IC packaging". ELECTRONICS PACKAGING TECHNOLOGY, 2003 pp. 551-556.			
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